

Form 1449*	Atty. Docket No.: 491.011US2	Serial No. Unknown <i>10/183/98</i>
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## U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<i>W</i>	3,611,309	10/05/1971	Zingg, R.J.	340	172.5	
<i>W</i>	3,975,712	08/17/1976	Hepworth, E.C., et al.	340	147	
<i>W</i>	4,342,082	07/27/1982	Brown, P.J., et al.	364	200	
<i>W</i>	4,486,750	12/04/1984	Aoki, T.	340	825	
<i>W</i>	4,785,393	11/15/1988	Chu, P.P., et al.	364	200	
<i>W</i>	5,274,770	12/28/1993	Khim Yeoh, C.W., et al.	395	275	
<i>W</i>	5,668,970	09/16/1997	Cowart, R., et al.	711	206	06/20/94
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## FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
<i>W</i>	0020185	12/10/1980	European	G06F	7/50	
<i>W</i>	0055370	07/07/1982	European	G06F	3/04	
<i>W</i>	0108664	05/16/1984	European	G06F	9/22	
<i>W</i>	0124402	11/07/1984	European	G06F	9/26	
<i>W</i>	0171190	02/12/1986	European	G06F	7/48	
<i>W</i>	0234187	09/02/1987	European	G06F	5/00	
<i>W</i>	0242003	10/21/1987	European	G06F	15/00	
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<i>W</i>	0394499	10/31/1990	European	G06F	7/52	
<i>W</i>	1435406	05/12/1976	United Kingdom	G06F	13/00	
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<i>W</i>	2142507	01/16/1985	United Kingdom	H04L	11/16	
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## OTHER DOCUMENTS

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<i>W</i>	"High-Speed Real-Time Event Processor", <u>IBM Technical Disclosure Bulletin</u> , Vol. 30, No. 2, IBM Corporation, New York, 632-634, (July 1987)
<i>W</i>	"Transfer of Subsystem Control Blocks and Termination Status Blocks Between System and Subsystem by Registers", <u>IBM Technical Disclosure Bulletin</u> , Vol. 37, No. 9, IBM Corporation, New York, 147-148, (Sept. 1994)
<i>W</i>	Clarke, P., "Mini MPU Aims at ASICs", <u>Electronic Engineering Times</u> , 24, (March 1991)

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\*Substitute Disclosure Statement Form (PTO-1449)

\*\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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